

iC9210-PN

Next Generation PLC

Ready-to-use system

- Triton 3 core processor (1.26 GHz ARM CPU)
- 2 - 8 MB program memory
- 4 - 16 MB data memory
- 128 - 512 kB retain memory

- I/O control
- PLC programming
- HMI
- PtP servo control
- Remote access, ...

- Linux based platform
- Programmable in IEC61131 or higher programming languages like C#, C++, Python, Matlab Simulink

- Easy handling in the i³ engineering tool

- Parallel collaboration on a project in different programming languages

100% compatible
with SLIO control system



iC9210-PN

Yaskawa Triton processor

3-core ARM processor with a clock speed of 1.26 GHz, an exclusive development for our high demand on synchronous motion and compactness with lower power dissipation than other chips

Network communication

OPC UA, EtherNet/IP, Modbus TCP, Profinet I-Device, I/O Link

Open programming

IEC61131-3 with PLCopen funktion blocks, C#, C++, Python, MATLAB Simulink

Modular design

Scalable performance on a real-time Linux system

Control system security

Network security certification according to ISA/IEC 62443



	iC9210-PN
Memory (Program / Data) [MB]	2 - 8 / 4 - 16 (can be configured flexibly)
Fieldbus	OPC UA Server, EtherNet/IP, Modbus TCP, Profinet I-Device, I/O Link
Connectors	Ethernet (2 × Gbit, 2 × 100Mbit), Serial port (1 × RS485), 24 V DC power supply (input), 21 W system power supply (output), SD memory card slot, USB-C (service only)
Extensibility	up to 64 I/O modules

YASKAWA Europe GmbH

Hauptstr. 185
65760 Eschborn
Deutschland

+49 6196 569-500
support@yaskawa.eu.com
www.yaskawa.eu.com

11/2021
YEU_i3_iC9210PN_Flyer_EN_v1

Due to continuous product modifications and improvements, specifications are subject to change without notice. © YASKAWA Europe GmbH. All rights reserved.

YASKAWA